

8C-TDIL01

Series 8 Digital Input Sequence-of-Events IOTA

Honeywell 8C-TDIL01 is a coated 32-circuit Series 8 digital-input sequence-of-events IOTA. It connects field contacts to the matched Series 8 SOE input processor.

SIGNAL AND EVENT-DATA PATH



PRODUCT SNAPSHOT

MANUFACTURER

Honeywell

PART NUMBER

51306856-175

PLATFORM

Experion Series 8 I/O

FUNCTION

Sequence-of-events digital input IOTA

TRADE ORIGIN

USA

HS / SHIPPING

8537101190 | Xiamen, Fujian, China

APTER POWER SALES TEAM

WHATSAPP / PHONE

+86 180 3023 5313

EMAIL

sales13@apterpower.com

WEBSITE

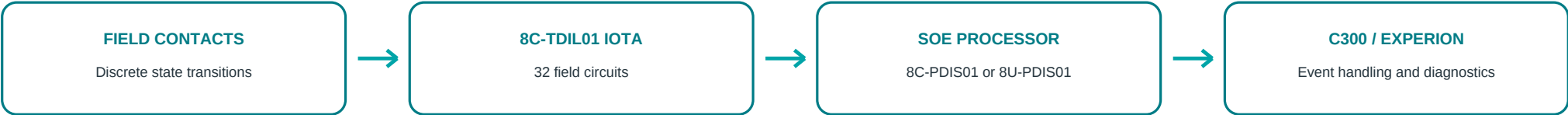
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8C-TDIL01

System role and event-data path

8C-TDIL01 is the field-termination interface in a Series 8 sequence-of-events input arrangement. It receives discrete field-contact wiring and connects it to the matched SOE processor module for ordered event acquisition.



Four model-specific functions

1

FUNCTION 1
Terminates sequence-of-events digital input circuits

2

FUNCTION 2
Connects the matched Series 8 processor module to field wiring

3

FUNCTION 3
Supports chronological capture of discrete state changes

4

FUNCTION 4
Provides a coated field-termination interface

EVENT-INTEGRITY CHECKS

- Preserve field-channel identity
- Synchronize the event timebase
- Verify the assigned SOE processor
- Prove ordered input transitions

Matched installation components

● Series 8 carrier

● 8C-PDIS01 or 8U-PDIS01

● Discrete field-contact wiring

● C300 event-processing configuration

ENGINEERING CONTEXT

Preserve channel identity from the field terminal through the assigned SOE processor. Event chronology depends on the complete installed arrangement, including the processor assignment and the C300 event-processing configuration.

Verified technical data

TECHNICAL PARAMETER	VERIFIED VALUE
IOTA function	Digital input sequence of events
I/O circuits	32
Coating	Conformal
Size code	9
Compatible modules	8C-PDIS01; 8U-PDIS01

Commissioning checkpoints

- 1

Confirm the complete 8C-TDIL01 and 51306856-175 markings.
- 2

Verify the assigned 8C-PDIS01 or 8U-PDIS01 processor position.
- 3

Retain field-channel identity while transferring terminal wiring.
- 4

Synchronize the event timebase before controlled transition tests.
- 5

Prove the chronological order of selected input transitions.

Identity and pairing matrix

TECHNICAL PARAMETER	VERIFIED VALUE
Complete model	8C-TDIL01
Manufacturer part	51306856-175
Product type	Digital input sequence-of-events IOTA
Platform	Experion Series 8 I/O
Processor pairing	8C-PDIS01 or 8U-PDIS01
Source publication	PC03-200-100

Technical note and acceptance record

Match the IOTA to the assigned SOE processor and preserve field-channel identity; after installation, synchronize the event timebase and prove transition order.

INSTALLED CARRIER / POSITION

ASSIGNED PROCESSOR

CHANNEL TEST RANGE

TIMEBASE / EVENT ORDER RESULT

Application map

1 TRIP-SEQUENCE RECORDING

Terminates sequence-of-events digital input circuits

2 SWITCHGEAR EVENT CHRONOLOGY

Connects the matched Series 8 processor module to field wiring

3 TURBINE AUXILIARY-STATE CAPTURE

Supports chronological capture of discrete state changes

4 PROCESS INTERLOCK DIAGNOSTICS

Provides a coated field-termination interface

Diagnostic workflow

IDENTIFY

Confirm IOTA, part marking and processor assignment.

ISOLATE

Control field energy and protect the electronic assembly.

RECORD

Capture channel identity and as-found connections.

RESTORE

Reinstall the matched Series 8 hardware arrangement.

PROVE

Inject controlled transitions and verify event order.

Installation and replacement controls

1

Verify the complete 8C-TDIL01 marking and its assigned Experion Series 8 I/O position before work.

2

Isolate the energy associated with Series 8 carrier before disconnecting 8C-TDIL01.

3

Protect the 8C-TDIL01 connectors and electronic assembly using approved ESD controls.

4

Record the as-found 8C-PDIS01 or 8U-PDIS01 connections and configured assignment before replacement.

5

Confirm Discrete field-contact wiring matches the approved installation record for 8C-TDIL01.

6

Restore 8C-TDIL01 under controlled conditions and review its model-specific diagnostics.

7

Complete a documented functional test for trip-sequence recording before release.

IMPORTANT NOTICE

Confirm the complete 8C-TDIL01 marking, Series 8 carrier position, processor pairing, field connections and event-processing configuration before commissioning or replacement. Preserve the documented channel identity and validate transition order before release.

Engineer FAQ

Q1 Which processor modules pair with 8C-TDIL01?

Use the documented Series 8 SOE processor pairing: 8C-PDIS01 or 8U-PDIS01.

Q2 How many field circuits does 8C-TDIL01 terminate?

The official Series 8 I/O specification lists 32 I/O circuits for this IOTA.

Q3 What signal role does 8C-TDIL01 perform?

It terminates discrete sequence-of-events input circuits and connects them to the assigned SOE processor.

Q4 Is 8C-TDIL01 a redundant IOTA?

8C-TDIL01 is the non-redundant variant; verify the installed architecture before selecting a replacement.

Q5 What should be recorded before replacing 8C-TDIL01?

Record the complete marking, carrier position, processor assignment, terminal wiring and channel identity.

Q6 How is 8C-TDIL01 commissioned after installation?

Synchronize the event timebase, apply controlled input transitions and confirm their chronological order.

Q7 What hardware must be checked with 8C-TDIL01?

Check the Series 8 carrier, matched SOE processor, field-contact wiring and C300 event-processing configuration.

Q8 What coating is specified for 8C-TDIL01?

The directly related Series 8 I/O specification lists conformal coating.

Related Series 8 products on Apter Power

MODEL	SERIES ROLE	APTER POWER PRODUCT PAGE
8C-PDIL51 51454359-175	Series 8 digital-input processor module	Open product page
8C-PDILA1	Series 8 digital-input processor module	Open product page
8C-PDOD51 51454361-175	Series 8 digital-output processor module	Open product page
8C-PDODA1 51454472-175	Series 8 24 Vdc digital-output module	Open product page
8C-TDODA1 51307149-175	Series 8 digital-output IOTA	Open product page
8C-TDODB1	Series 8 redundant digital-output IOTA	Open product page