MC-PDIS12

Sequence-of-Events Digital Input Processor

Honeywell MC-PDIS12 is a coated 32-point sequence-of-events digital input processor. It collects discrete changes and delivers event-oriented input data to TPS Process Manager control functions.

PRODUCT SNAPSHOT

BRAND	Honeywell
FAMILY	TPS Process Manager I/O
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PRODUCT OVERVIEW

Honeywell MC-PDIS12 is a coated 32-point sequence-of-events digital input processor. It collects discrete changes and delivers event-oriented input data to TPS Process Manager control functions.

1. Acquires discrete field-state transitions
2. Time-orders input transitions for event review
3. Transfers digital status through the I/O link
4. Uses a conformally coated processor board

SYSTEM COMPONENTS

- Compatible digital-input FTA
- PM/APM/HPM/EHPM I/O cardfile
- I/O link
- Discrete field contacts

TECHNICAL DATA

PARAMETER	VALUE
Input points	32
Input function	Sequence of events

PARAMETER	VALUE
Board coating	Conformal
Processor family	PM/APM/HPM/EHPM I/O

TECHNICAL NOTE

Match the input FTA and I/O link assignment, synchronize the system time source and test channel transition order before enabling event collection.

APPLICATIONS & PRECAUTIONS

COMMON APPLICATIONS

Trip-event chronology

Acquires discrete field-state transitions

Turbine auxiliary event capture

Time-orders input transitions for event review

Switchgear status sequencing

Transfers digital status through the I/O link

Process interlock diagnostics

Uses a conformally coated processor board

PRODUCT PRECAUTIONS

1. Verify the complete MC-PDIS12 marking and the assigned TPS Process Manager I/O position before field work.
2. Isolate the applicable power, network and field energy before disconnecting MC-PDIS12.
3. Use ESD controls and protect the keyed connectors of MC-PDIS12 during handling.
4. Record the installed wiring, jumpers, node or channel assignment before replacement.
5. Confirm Compatible digital-input FTA and PM/APM/HPM/EHPM I/O cardfile against the approved system configuration.
6. Restore MC-PDIS12 under controlled conditions and review the product-specific diagnostics.
7. Complete a functional test for trip-event chronology before production release.

ENGINEER FAQ

Q1 Which Compatible digital-input FTA identity must be verified before fitting the MC-PDIS12 Sequence-of-Events Digital Input Processor?

Match the complete MC-PDIS12 marking, assigned TPS Process Manager I/O position and installed Compatible digital-input FTA before work.

Q2 How should PM/APM/HPM/EHPM I/O cardfile be isolated before disconnecting the MC-PDIS12 Sequence-of-Events Digital Input Processor?

Place the affected process in its approved maintenance state, isolate applicable energy and prove PM/APM/HPM/EHPM I/O cardfile safe before disconnection.

Q3 What as-found wiring record is required for the MC-PDIS12 Sequence-of-Events Digital Input Processor in Trip-event chronology?

Record terminal identity, polarity, shields, jumpers, cable tags and the as-found diagnostic state for MC-PDIS12.

Q4 Which TPS Process Manager I/O configuration must be checked when commissioning the MC-PDIS12 Sequence-of-Events Digital Input Processor?

Verify the assigned channel, slot, node or interface and compare it with the approved TPS Process Manager I/O configuration before enabling MC-PDIS12.

Q5 How can the MC-PDIS12 Sequence-of-Events Digital Input Processor be function-tested for Turbine auxiliary event capture without disturbing the process?

Use a controlled stimulus or limited command, observe the expected system indication and confirm the intended response while normal interlocks remain effective.

Q6 Which diagnostics should be reviewed if the MC-PDIS12 Sequence-of-Events Digital Input Processor stops performing its assigned system function?

Check seating, the matched Compatible digital-input FTA, the condition of PM/APM/HPM/EHPM I/O cardfile and the model-specific diagnostics before replacing more hardware.

Q7 Can the MC-PDIS12 Sequence-of-Events Digital Input Processor used for Switchgear status sequencing be transferred to another TPS Process Manager I/O position without engineering review?

No. Confirm the hardware execution, connector scheme and approved TPS Process Manager I/O configuration before transferring MC-PDIS12 to a different duty.

Q8 What evidence should be retained after the MC-PDIS12 Sequence-of-Events Digital Input Processor returns to Process interlock diagnostics service?

Retain the physical identification, as-left wiring or configuration, final diagnostics and controlled functional-test result for MC-PDIS12.

SOURCE Honeywell TPS Process Manager I/O Specification | IO03-684 Rev.2

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