

151X1202YE03SA04 IS200STAIH2ADC

Installation and Verification Reference

GE 151X1202YE03SA04 IS200STAIH2ADC is the requested full nameplate for an IS200STAIH2A-family Mark VIe simplex analog I/O terminal board. GE Vernova official certificates list IS200STAIH2A with the IS220PAICH1B or IS42yPAICH1C analog I/O pack. The terminal board provides the field-wiring interface for process analog input and output circuits; the ADC suffix and 151X1202YE03SA04 marking must be verified on the physical unit because the cited official PDFs do not separately decode them.

Manufacturer	General Electric / GE Vernova
Complete model	151X1202YE03SA04 IS200STAIH2ADC
Product description	Mark VIe Simplex Analog I/O Terminal Board
Prepared by	Apter Power
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151X1202YE03SA04
IS200STAIH2ADC

1. System role

Installed in a compatible Mark VIe control panel, the STAI terminal board terminates analog field wiring and routes the field circuits to the documented PAIC I/O pack. The I/O pack performs signal conversion, processing, diagnostics and controller communications; the terminal board itself does not execute turbine or process-control logic.

What the product handles

Process analog transmitter input and analog output field circuits used by the documented PAIC analog I/O module group. The exact ADC-revision channel count, signal ranges, terminal allocation and transmitter-wiring arrangement are not stated in the cited exact-family certificate and are intentionally not inferred.

Required companion components

GE Vernova documents the IS200STAIH2A base with the IS220PAICH1B or IS42yPAICH1C analog I/O pack. Use only the applicable Mark VIe controller, panel hardware, terminal accessories, field wiring, power arrangement and project configuration approved for the installed system.

Where it is used

Mark VIe power-generation and industrial control panels that require a simplex field-terminal interface between analog process transmitters or actuators and a compatible PAIC I/O pack.

Pre-installation record

Parameter	Verified value
Identity	Record complete model 151X1202YE03SA04 IS200STAIH2ADC and every revision/suffix from the nameplate
Evidence	Retain the named manufacturer publication and site-approved drawings
Configuration	Back up the approved controller, network or module configuration
Connections	Label cables, terminals, module positions and field wiring
Commissioning	Define a point-by-point or communication verification plan

2. Installation and commissioning controls

Recommended verification sequence

Parameter	Verified value
1 - Identify	Match the complete nameplate and physical construction
2 - Isolate	Follow site lockout, electrical-safety and ESD procedures
3 - Compare	Confirm companion hardware, cables, termination and revision compatibility
4 - Install	Use manufacturer mounting, grounding and environmental requirements
5 - Configure	Restore only the approved configuration and documented settings
6 - Test	Verify communications, diagnostics and every affected process or safety function
7 - Release	Record results and obtain the required site authorization

Model-specific precautions

- Match the complete 151X1202YE03SA04 IS200STAIH2ADC nameplate, board revision and installed Mark VIe project bill of materials before use.
- Use only the GE-documented PAIC I/O pack and the exact terminal, cable, power and grounding arrangement approved for the installed panel.
- Do not copy channel counts, signal ranges, terminal assignments, dimensions, weight or country of origin from a sibling STAI revision.
- De-energize the panel and field circuits and follow the site's approved electrical-safety, process-safety and ESD procedures before handling the board.
- Record every conductor, shield and terminal before removal, then verify polarity, grounding, cabling and intrinsic-safety documentation against the approved panel drawings.
- For the documented certified I/O-pack system group, verify Uo 28.6 V dc, Io 22.4 mA, Po 0.641 W, Co 0.255 uF and Lo 100 mH against the applicable GE certificate and site design.

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