

DS200DTBBG1ABB

SPEEDTRONIC Mark V DTBB Digital Contact Input Terminal Board

The General Electric DS200DTBBG1ABB is identified by its DTBB family marking as a SPEEDTRONIC Mark V digital contact-input terminal board. The cited Mark V documentation places DTBB in the control-panel terminal-board architecture; the full G1ABB hardware revision must be matched to the installed board and unit-specific drawings.

Manufacturer	General Electric
Complete model	DS200DTBBG1ABB
Product description	SPEEDTRONIC Mark V DTBB Digital Contact Input Terminal Board
Prepared by	Apter Power
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SCAN FOR PRODUCT & QUOTE

DS200DTBBG1ABB

1. Product overview and identity

The General Electric DS200DTBBG1ABB is identified by its DTBB family marking as a SPEEDTRONIC Mark V digital contact-input terminal board. The cited Mark V documentation places DTBB in the control-panel terminal-board architecture; the full G1ABB hardware revision must be matched to the installed board and unit-specific drawings.

System function

DTBB provides field termination for a bank of Mark V digital contact inputs and routes those signals to the compatible TCDA digital I/O board for conditioning, change-of-state time tagging and transfer over IONET. The terminal board does not execute turbine control logic; the Mark V control processors use the resulting digital states in the configured sequencing and protection application.

Verified technical information

Parameter	Verified value
Complete model	DS200DTBBG1ABB
Board family	DTBB digital contact-input terminal board
Control platform	SPEEDTRONIC Mark V
Documented signal group	Contact inputs 47 through 96
Documented input range	24 to 125 VDC
Signal processor companion	TCDA digital I/O board
System data path	Field contacts to DTBB/TCDA, then IONET to Mark V control
Revision boundary	G1ABB compatibility must be verified from nameplate and unit drawings

2. Integration and verification

Signals and data handled

Digital contact inputs for the Mark V control panel. Mark V hardware references identify DTBB with contact-input signal numbers 47 through 96 and a documented input range of 24 to 125 VDC; use the unit-specific drawings for the actual terminal assignment and wetting source.

Companion components

Mark V TCDA digital I/O board, QD/CD core hardware as installed, ribbon cables and power distribution, field switches/relay contacts, IONET communications and unit-specific I/O assignment drawings.

Common industrial applications

Gas- and steam-turbine Mark V panels that terminate field status, permissive, alarm or trip-related contact inputs used by the configured turbine-control application.

Product precautions

- Use the unit-specific I/O assignment and wiring drawings; do not move a field wire based only on a generic DTBB terminal number.
- Isolate the Mark V contact-input wetting supply and verify absence of voltage before disconnecting the board or field wiring.
- After installation, verify TCDA communications, every affected contact state, alarm logic and trip permissive under an approved test plan.

Manufacturer publications

Manufacturer publication	Source access
GEH-5980E SPEEDTRONIC Mark V Turbine Control Maintenance Manual	Open source in a new window
GE Mark V / Mark VIe migration type-approval record (DTBB family reference)	Open source in a new window

Only information traceable to the named manufacturer publications is stated. Missing electrical, dimensional, lifecycle, origin and compatibility values are intentionally not inferred. Confirm the complete physical nameplate, revision, approvals, wiring and current manufacturer instructions before use.