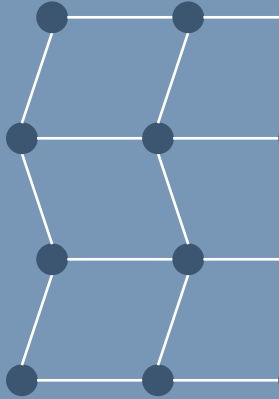


MANUFACTURER-SOURCE TECHNICAL REFERENCE

General Electric IS200TDBSH2A-IS200TDBSH2AAA

IS200TDBSH2A Requested-Label Verification Evidence-Bounded Product Information Manual

GE Vernova Mark VIe terminal-board identity with requested full-suffix
verification



DOCUMENT SCOPE

Independent Apter Power manual compiled only from the cited GE Vernova manufacturer sources. No product photograph is included.

Product / lookup identity	General Electric IS200TDBSH2A-IS200TDBSH2AAA
Reference number	IS200TDBSH2A
Document basis	Published GE Vernova product records, PDFs, and manuals listed on page 3



Product page

<https://www.apterpower.com/General-Electric/is200tdbsh2a>
Apter Power contact

sales13@apterpower.com | +86 180 3023 5313

Workflow and system context

General Electric IS200TDBSH2A is an evidence-bounded Mark VIe terminal-board identity with requested full-suffix verification. System role: Official Mark VIe certification pairs IS200TDBSH2A with IS220PDIOH1A or IS220PDIOH1B in the documented discrete-I/O architecture.

- | | |
|----|---|
| 01 | System Inputs: Official Mark VIe certification pairs IS200TDBSH2A with IS220PDIOH1A or IS220PDIOH1B in the documented discrete-I/O architecture. |
| 02 | Processing: Mark VIe I/O is organized as terminal board, terminal block and I/O pack; preserve the exact installed combination, revision and approved certification conditions. |

Published context and boundaries

- | | |
|----|---|
| 01 | Verify the complete General Electric IS200TDBSH2A physical label and installed-system documentation before use. |
| 02 | Do not treat IS200TDBSH2AAA as a confirmed alias or revision. Do not publish analog-I/O, memory, communication or generic environmental values from unrelated boards. |
| 03 | Origin and HS code remain unpublished unless an exact manufacturer record or the physical package label establishes them. |

Verification and release checks

01	Verify the complete General Electric IS200TDBSH2A label, revision and installed-system documentation before use.
02	Do not treat IS200TDBSH2AAA as a confirmed alias or revision. Do not publish analog-I/O, memory, communication or generic environmental values from unrelated boards.
03	De-energize the equipment and follow the site's approved electrical and process-safety procedure before disturbing hardware or wiring.
04	Back up controller, drive, network and configuration data before replacement, migration or firmware work.
05	Verify every companion module, cable, connector, termination and power source from approved drawings.
06	Do not copy electrical, communication, mechanical or lifecycle values from a related model or successor.

Identity and release controls

Verification Status	Requested Suffix Verification
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Official GE Vernova sources

Mark Vle Control Product Description	https://www.gevernova.com/content/dam/gepower-new/global/en_US/downloads/gas-new-site/resources/reference/GEA35311-Mark-Vle-Control-Product-Description-Brochure.pdf
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