Motorola MVME5100

Complete Product Information | decision matrix

Motorola MVME5100 is a PowerPlus II VME processor module with dual PMC positions, integrated Ethernet and serial interfaces, and configurable P2 I/O modes.

02 Configuration and interface matrix

Architecture	Motorola PowerPlus II VME
Base expansion	Two IEEE P1386.1 PMC positions
Ethernet	Dual 10/100BaseT interfaces in series architecture
Serial	Two 16550-compatible asynchronous ports in series architecture
P2 modes	Jumper-selectable PMC or IPMC
Rear I/O options	MVME761 or MVME712M through matching IPMC/adapter
Memory protection	ECC SDRAM architecture; installed capacity depends on full order code
Startup	On-board firmware monitor
Processor identity	Read complete order suffix before assigning CPU type or clock

03 Application roles and compatibility

VME compute host

Run the existing processor application with its original firmware and VME bus configuration.

PMC customization

Retain fitted PMC cards and their front or P2 field connections when the base board is exchanged.

Legacy rear I/O

Use MVME761 or MVME712M paths only with the matching IPMC module and adapter arrangement.

Network and serial service

Restore the intended Ethernet and console connections, then validate the application protocol after boot.

Practical compatibility boundary

MVME5100 is a family-level marking. Motorola lists different full order configurations for processor, memory and temperature class, so none of those variant values is assigned to this unqualified unit.

04 Product precautions

Identify variant

Read the complete board order code and revision before selecting CPU or memory-specific software.

Disconnect safely

Isolate chassis power and mark front/rear cables before extracting the board.

P2 jumper mode

Record PMC or IPMC selection and compare it with the transition hardware.

Mezzanine population

Document RAM500, PMC and IPMC cards and their exact slot positions.

05 Offered unit and lifecycle

Condition: New, original product. Confirm the complete board label, fitted RAM500 and IPMC/PMC modules, P2 mode and transition hardware in the quotation; host software and rear cabling are separate.

Lifecycle status

MVME5100: A current manufacturing or support phase for this complete marking is unconfirmed. For support planning, first establish the complete order suffix, P2 mode and installed mezzanines. Apter Power availability is separate from the manufacturer lifecycle phase.

Engineering decisions

What must be known before a CPU-specific spare is chosen for MVME5100?	Read the full order suffix from the installed board. MVME5100 family literature covers multiple processor, memory and temperature builds.
How do PMC and IPMC modes change MVME5100 rear-I/O compatibility?	The P2 jumper selection routes signals for different daughtercard arrangements. Match mode, populated card and transition adapter as one system.
When can MVME5100 work with an MVME761 transition module?	The required IPMC761 and matching P2 adapter must be fitted, with the supported cable and jumper arrangement; the processor board alone is insufficient.
Why might a PMC function be absent after replacing MVME5100?	Check the card population and seating, PCI/PMC recognition, P2 mode and driver image before blaming the base board.
How should MVME5100 boot data be protected?	Capture monitor settings, storage path and network parameters; verify the replacement uses the correct software image for its complete board configuration.

Discuss the fitted revision.
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Source: Motorola MVME5100 Series VME Processor Modules, M5100-D13, 12/02

Other Motorola references

Motorola MVME2304 64-W5206C01B

Motorola MVME31006E-1152

Motorola MVME172-333

Catalog navigation only; verify each model separately.