Motorola MVME2304 64-W5206C01B

Technical Reference | exact-model engineering dossier

Motorola MVME2304 64-W5206C01B is an MVME2300-series PowerPC VME processor board with dual PMC expansion sites, front-panel Ethernet and an asynchronous serial interface.

Exact offered identity	MVME2304 64-W5206C01B
Engineering role	VMEbus PowerPC Processor Module
Installed-system reference	MVME2300 PowerPlus VME processor platform

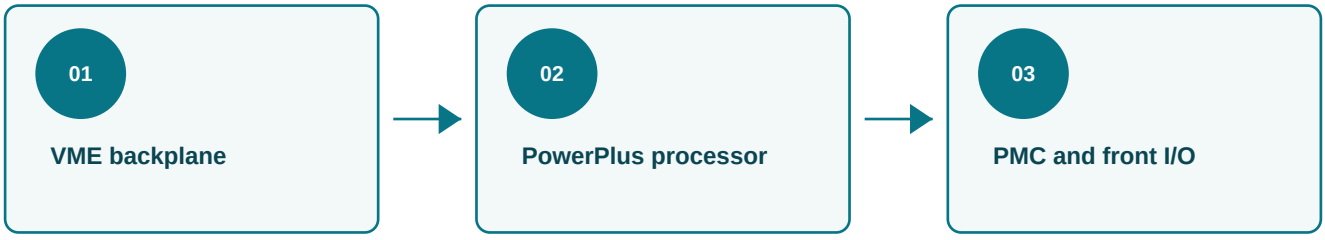
Functional identity

The Motorola series brochure identifies MVME2304 and the shared MVME2300 hardware architecture. The additional 64-W5206C01B marking is retained as supplied; its processor-speed and memory configuration are not decoded from the family brochure.

Engineering decision points

Match the complete marking	Include MVME2304 64-W5206C01B and every visible revision or serial identity in your enquiry. Keep the same complete reference on the quotation.
Check the offered item before ordering	Request available label and connector photos. Have your maintenance team compare the offered MVME2304 64-W5206C01B with the installed equipment and applicable Motorola documentation.

Engineering path



Source basis

Motorola MVME2300 Series VME Processor Modules, M2300-D6

02 System position and boundaries

VME chassis control

Use the processor module as the host for software and VME transactions in a chassis whose backplane and controller selection match the board.

PMC expansion

Retain the installed PMC population and connector routing when transferring a machine application to a replacement board.

Network service

Use the front-panel Ethernet path where the installed network plan provides its address and boot configuration.

Serial diagnostics

Use the asynchronous front-panel port for supported service or console access, with the correct cable and terminal settings.

Installed system

Confirm the board marking, fitted memory and PMC modules in the quotation; backplane, rear I/O hardware and software image are separate installation elements.

Match both MVME2304 and 64-W5206C01B on the physical label before treating an offered board as the requested assembly.

03 Verified technical data

Processor family	PowerPC 603 or 604 within MVME2300 series; inspect fitted board
VME interface	VME64 extension, 5-row P1/P2 connectors
Expansion	Two IEEE P1386.1 PMC sites
Network interface	10BaseT/100BaseTX Ethernet via front-panel RJ-45
Serial interface	One asynchronous serial port via front-panel RJ-45
Memory architecture	ECC-protected DRAM; capacity depends on assembly
Nonvolatile storage	Flash and battery-backed NVRAM in series architecture
System-control option	Jumper-selectable or automatic VME system controller
Rear I/O boundary	PMC P2 routing requires a compatible VME64 extension backplane

Review sequence



04 Integration and diagnostic review

Board identity

Match both MVME2304 and 64-W5206C01B on the physical label before treating an offered board as the requested assembly.

I/O routing

The two PMC sites can use front-panel or P2 I/O. Verify the fitted PMC modules and backplane pinout before connecting rear I/O.

Power isolation

Switch off the VME chassis and verify discharged power rails before extracting the processor.

Backplane seating

Inspect P1/P2 engagement and the 5-row extension arrangement; never force a module into a mismatched connector.

Acceptance sequence

STEP	ACTION	ACCEPTANCE
01	System controller	Compare the controller jumper or auto-detect setting with the original chassis configuration.
02	Return-to-service test	Prove boot, Ethernet and serial access, then test each application-specific PMC path under the machine acceptance plan.
03	Record the functional result	Run the installed software image, confirm chassis controller operation, exercise network and serial service, and prove every fitted PMC function through its real field connection.

05 Engineering questions and source record

How should MVME2304 64-W5206C01B be matched to an installed VME slot?	Compare both markings, P1/P2 connector style, controller selection and fitted PMC population. A base MVME2304 label alone does not establish assembly equivalence.
What determines rear I/O compatibility for MVME2304 64-W5206C01B?	Rear PMC signals use the P2 path only when the chosen mezzanine and VME64 extension backplane support that routing. Check the original slot wiring before connecting a transition module.

How should the CPU build of MVME2304 64-W5206C01B be verified?	Read the offered board labels and boot identification; the MVME2300 family contains more than one processor and speed combination.
What should be preserved before exchanging MVME2304 64-W5206C01B?	Record the boot media, firmware settings, network address, controller mode and any populated PMC modules so the replacement can be commissioned against the same system baseline.
Why might a PMC device disappear after installing MVME2304 64-W5206C01B?	Check the mezzanine seating and PCI configuration first, then confirm that its expected front-panel or P2 signal route is present in the chassis.
How is the front-panel network path of MVME2304 64-W5206C01B checked?	Verify cable link and configured network parameters, then test the application endpoint. A lit link indicator alone does not prove the correct boot image or host communication.

Document identity

Apter Power technical reference based on Motorola MVME2300 Series VME Processor Modules, M2300-D6. The exact offered code and installed revision govern applicability.

Discuss the complete model and fitted revision with Apter Power.
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Other Motorola references

Catalog navigation only; confirm each complete model for its own installation.

Motorola MVME5100	View product reference
Motorola MVME31006E-1152	View product reference
Motorola MVME172-333	View product reference