

COMPLETE PRODUCT INFORMATION

214-2BM02

PLC CPU Module

VIPA 214-2BM02 is a System 200V CPU 214DPM that combines a programmable controller with an integrated PROFIBUS-DP master. It provides 48 KB work memory, 80 KB load memory and a 24 VDC supply interface.

VERIFIED IDENTITY

VIPA | System 200V | Country of origin: Germany

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214-2BM02

Technical Data

PARAMETER	VERIFIED VALUE
Catalog number	214-2BM02
CPU designation	CPU 214DPM
Work memory	48 KB
Load memory	80 KB
Supply	24 VDC; 20.4 to 28.8 VDC
Current consumption	Maximum 1.5 A
Power dissipation	Maximum 5 W
Fieldbus	PROFIBUS-DP master, 9.6 kbit/s to 12 Mbit/s
Connector	9-pin D-sub
Maximum PROFIBUS slaves	125
Process data	1024 input bytes and 1024 output bytes
Country of origin	Germany

Functions and Applications

CORE FUNCTION 1

PLC and PROFIBUS-DP master in one CPU

CORE FUNCTION 2

48 KB work and 80 KB load memory

CORE FUNCTION 3

PROFIBUS rates up to 12 Mbit/s

CORE FUNCTION 4

System 200V modular expansion

APPLICATION	ENGINEERING CONTEXT
Distributed machine control	Executes the PLC program while coordinating documented PROFIBUS-DP slave stations.
Remote I/O networks	Exchanges up to the documented process-data capacity with approved DP slaves.
System 200V modernization	Maintains the exact CPU 214DPM identity in a modular control station.
PROFIBUS diagnostics	Provides master diagnostics for node, wiring and configuration checks.

Installation and Precautions

- Verify the complete 214-2BM02 marking and CPU 214DPM designation.
- Back up the PLC program, hardware configuration and PROFIBUS master data.
- Confirm the 24 VDC source is within the documented range.
- Record every PROFIBUS address, baud rate and slave configuration.
- Remove power before inserting or removing System 200V modules.
- Maintain PROFIBUS shielding, connector orientation and end termination.
- Do not exceed the approved current, memory or process-data limits.
- Verify CPU status, bus diagnostics and controlled I/O operation before release.

ENGINEERING NOTE

Back up the PLC program and PROFIBUS master configuration before replacement. Verify the 24 VDC source, station addresses, bus termination, cable shield and documented slave configuration before returning the System 200V station to service.

Model-Specific FAQ and Support

QUESTION	ANSWER
What is VIPA 214-2BM02?	It is a System 200V CPU 214DPM combining PLC functions with an integrated PROFIBUS-DP master.
How much memory is provided?	The VIPA manual lists 48 KB work memory and 80 KB load memory.
What supply does 214-2BM02 require?	The documented supply is 24 VDC with a 20.4 to 28.8 VDC operating range.
What PROFIBUS speeds are supported?	The integrated master supports documented rates from 9.6 kbit/s through 12 Mbit/s.
How many PROFIBUS slaves are supported?	The CPU manual specifies up to 125 DP slaves, subject to the approved network design.
What process-data capacity is documented?	The manual specifies 1024 input bytes and 1024 output bytes for the PROFIBUS master.
What must be preserved before replacement?	Save the PLC program, hardware configuration, station addresses and bus parameters.
What confirms successful commissioning?	The CPU reaches the intended operating state, PROFIBUS diagnostics are clear and controlled I/O tests pass.

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