

COMPLETE PRODUCT INFORMATION

DS200TCQAG1BFD-DS215TCQAG1BZZ01A

TCQA I/O Board

General Electric DS200TCQAG1BFD-DS215TCQAG1BZZ01A identifies a TCQA analog I/O board assembly for the SPEEDTRONIC Mark V control platform. The complete paired identifier must be retained when matching an installed board and its documented revision.

VERIFIED IDENTITY

General Electric | SPEEDTRONIC Mark V | Country of origin: USA

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Technical Data

PARAMETER	VERIFIED VALUE
Complete model	DS200TCQAG1BFD-DS215TCQAG1BZZ01A
Board designation	TCQA
Product role	Analog I/O board assembly
Control family	SPEEDTRONIC Mark V
Installed location	Documented Mark V R, S or T control core
Terminal-board relationship	Installed-system TBQA/TBQB wiring record
Replacement control	Match both DS200 and DS215 identifiers
Country of origin	USA

Functions and Applications

CORE FUNCTION 1

TCQA analog I/O board function

CORE FUNCTION 2

Mark V control-core integration

CORE FUNCTION 3

Terminal-board signal interface

CORE FUNCTION 4

Complete dual-identifier revision control

APPLICATION	ENGINEERING CONTEXT
Mark V analog signal processing	Maintains the documented analog input and output path in a SPEEDTRONIC Mark V control core.
Turbine control maintenance	Supports controlled replacement of the recorded TCQA board assembly.
Core-level diagnostics	Provides the board identity required for approved Mark V diagnostic and calibration checks.
Legacy system support	Preserves the exact DS200 and DS215 identifiers used by the installed control record.

Installation and Precautions

- Verify the complete DS200TCQAG1BFD-DS215TCQAG1BZZ01A marking before work.
- Place the turbine and Mark V control in the approved maintenance state.
- Record the R, S or T core location and every connected cable before removal.
- Use ESD controls and handle the TCQA board only by its edges.
- Record socketed devices, jumpers and revision markings without changing them.
- Do not substitute a visually similar TCQA suffix without GE documentation.
- Seat the board and latching cable connectors without force or offset.
- Run the applicable Mark V diagnostics and verify signal response before release.

ENGINEERING NOTE

Record the full DS200TCQAG1BFD-DS215TCQAG1BZZ01A marking, core location, terminal-board cables and socketed devices before removal. Reinstall only in the documented Mark V location and complete the approved diagnostic checks before release.

Model-Specific FAQ and Support

QUESTION	ANSWER
What is GE DS200TCQAG1BFD-DS215TCQAG1BZZ01A?	It is a TCQA analog I/O board assembly identified for the SPEEDTRONIC Mark V control platform.
Why must both identifiers be retained?	The paired DS200 and DS215 markings define the supplied assembly and revision record; shortening the identity can cause a mismatch.
Where is the TCQA used?	GE Mark V documentation places TCQA boards in the control-core analog I/O architecture; the exact R, S or T location must come from the installed record.
Which terminal boards are associated with the TCQA path?	Mark V documentation describes TCQA signal paths with the applicable TBQA and TBQB terminal-board wiring.
Can another TCQA suffix replace this assembly?	Not by appearance alone. The complete suffix, revision, socketed devices and site configuration require documented approval.
What should be recorded before removal?	Record the full marking, core position, cable destinations, socketed devices, jumpers and diagnostic state.
What ESD practice is required?	Use a grounded ESD workstation and transport the board in approved static-protective packaging.
What confirms successful commissioning?	The board is correctly seated, diagnostics pass, and the affected analog signals agree with the approved operating record.

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