

APTER POWER INSTALLATION & VERIFICATION REFERENCE

6ES7217-1AG40-0XB0

A separate model-specific field checklist

Use this reference to verify the exact Siemens component before installation, substitution, or recommissioning. It intentionally does not repeat unconfirmed values.

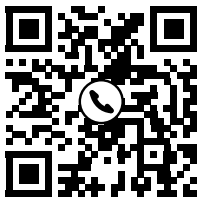
Manufacturer	Siemens
Prepared by	Apter Power
Document ID	AP-SIE-14-6ES7217-1AG40-0XB0-IV-R1
Revision	R1
Issue date	2026-08-20

APTER POWER SALES TEAM

INDUSTRIAL AUTOMATION PRODUCT SUPPORT

Differential high-speed I/O. Mixed analog control.
The broadest compact S7-1200 CPU.

WHATSAPP / PHONE +86 180 3023 5313
EMAIL sales13@apterpower.com
WEBSITE www.apterpower.com



SCAN FOR PRODUCT & QUOTE
6ES7217-1AG40-0XB0

1. Pre-Installation Verification

Step	Check	Required evidence
1	Identity	Match every character of 6ES7217-1AG40-0XB0 to the physical label; record revision and serial data.
2	Host system	Confirm rack, slot, CPU/drive/controller, firmware, project, and machine-builder approval.
3	Electrical	Verify supply, signal type, channel limits, grounding, shielding, and connector pinout from the exact Siemens document.
4	Mechanical	Confirm form factor, keying, mounting, clearances, cable routing, and environmental limits.
5	Lifecycle	Siemens Industry Mall lists the complete article number as a PM300 Active Product. Confirm the current regional lifecycle, delivery status, firmware, and engineering-software requirements when quoting.
6	Commissioning	Apply the original test procedure and record results before returning the equipment to service.

Product-Specific Precautions

- Confirm every character of 6ES7217-1AG40-0XB0, the CPU 1217C DC/DC/DC marking, hardware revision, and firmware against the TIA Portal project.
- Isolate power and verify 24 V DC; 20.4 to 28.8 V DC; follow Siemens conductor, terminal, grounding, and circuit-protection requirements.
- Check 24 V DC output polarity, 0.5 A channel limit, group current, pulse-output frequency, and load suppression before connecting field loads.
- Separate analog and communication wiring from power conductors; terminate shields and references according to the S7-1200 system manual.
- Restore the verified project, commission inputs and outputs by circuit group, test network and fault behavior, and record the results.

2. Commissioning Record

Record item	Site entry
Physical model / MLFB	
Serial or revision	
Host system / machine	
Rack / slot / location	
Firmware / project backup	
Connector and wiring verified	
Functional test result	
Technician / date	

Source Basis

Siemens official technical data sheet for 6ES7217-1AG40-0XB0: <https://support.industry.siemens.com/teddatasheet/?caller=SIOS&format=pdf&language=en&mlfbs=6ES7217-1AG40-0XB0>; Siemens official S7-1200 System Manual landing page: <https://support.industry.siemens.com/cs/ww/en/view/109759862>

Important Notice

This Apter Power checklist is not a Siemens installation instruction. The current Siemens documentation, the physical nameplate, the host-equipment manual, and applicable safety rules control all work.

APTER POWER SALES TEAM

INDUSTRIAL AUTOMATION PRODUCT SUPPORT

**Differential high-speed I/O. Mixed analog control.
The broadest compact S7-1200 CPU.**

WHATSAPP / PHONE +86 180 3023 5313

EMAIL sales13@apterpower.com

WEBSITE www.apterpower.com



SCAN FOR PRODUCT & QUOTE

6ES7217-1AG40-0XB0